

Fig 1

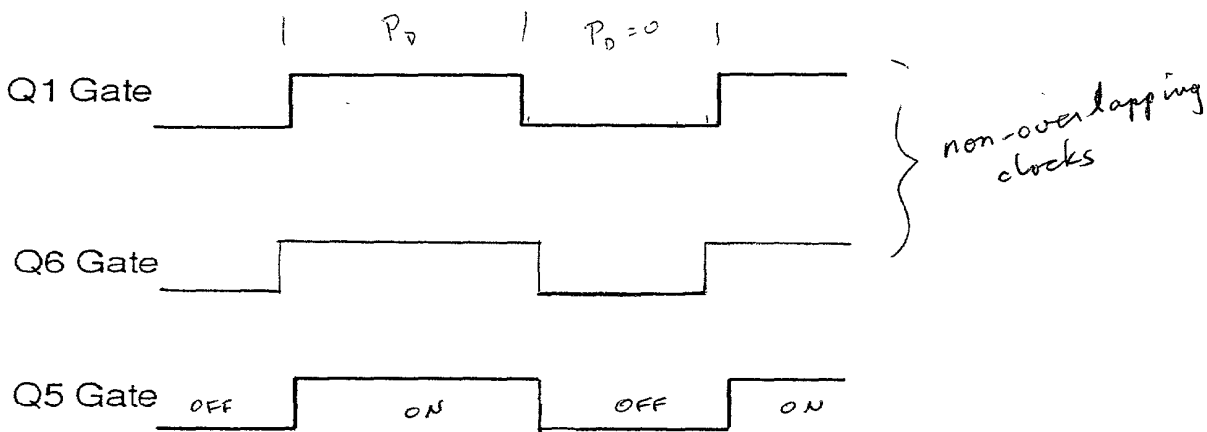
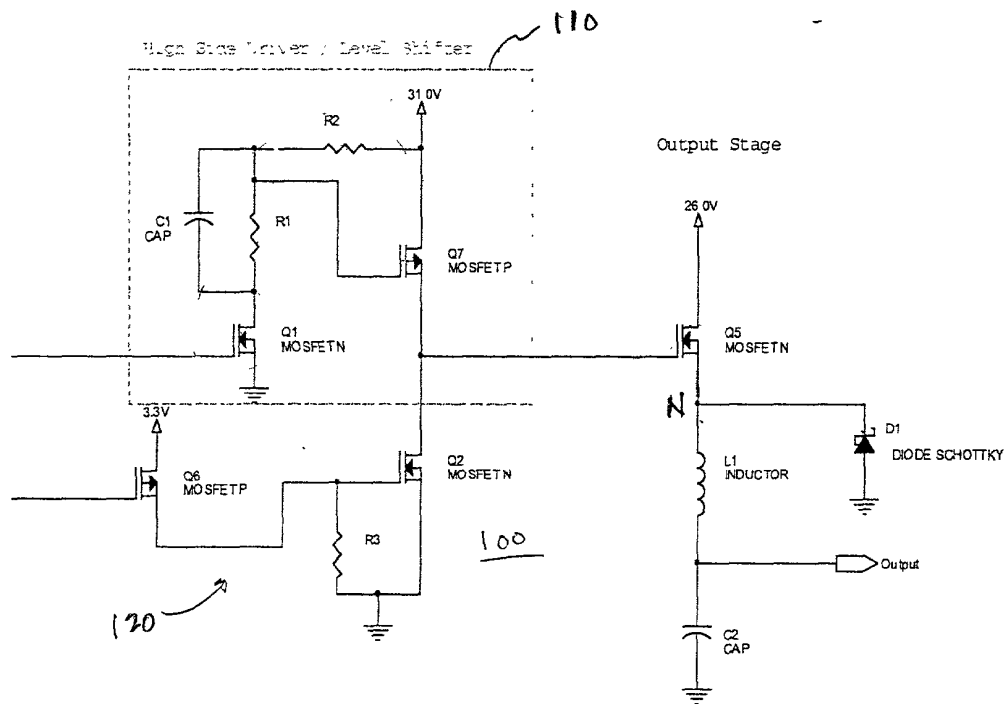


Fig 2

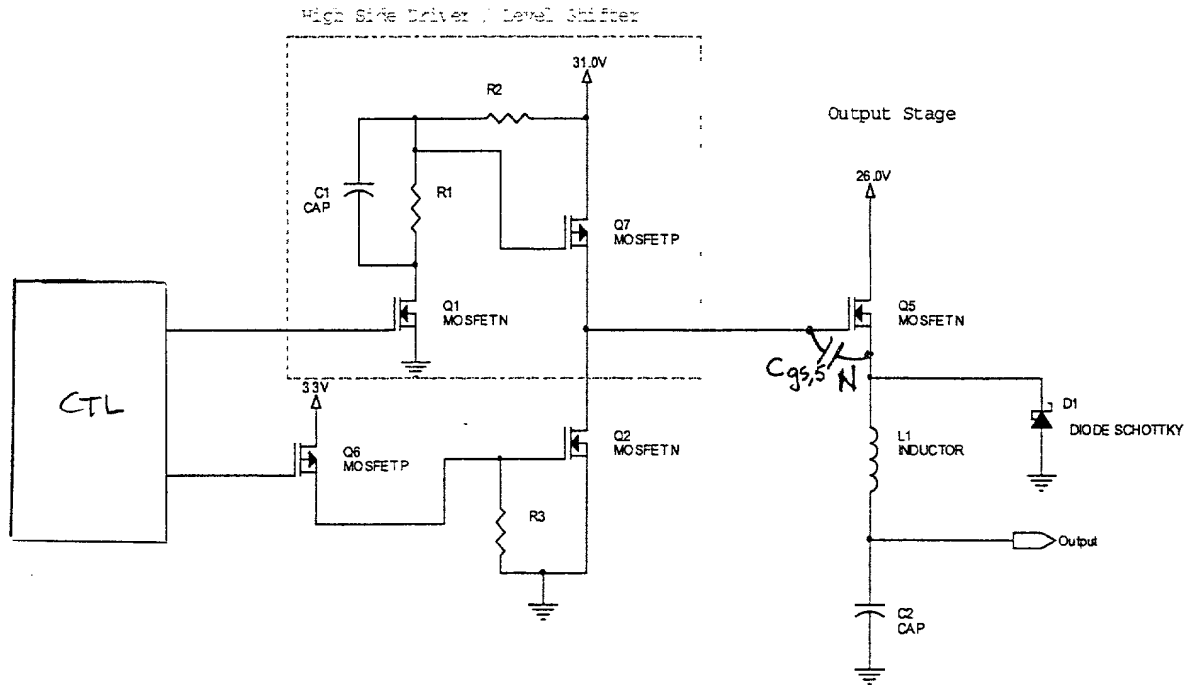
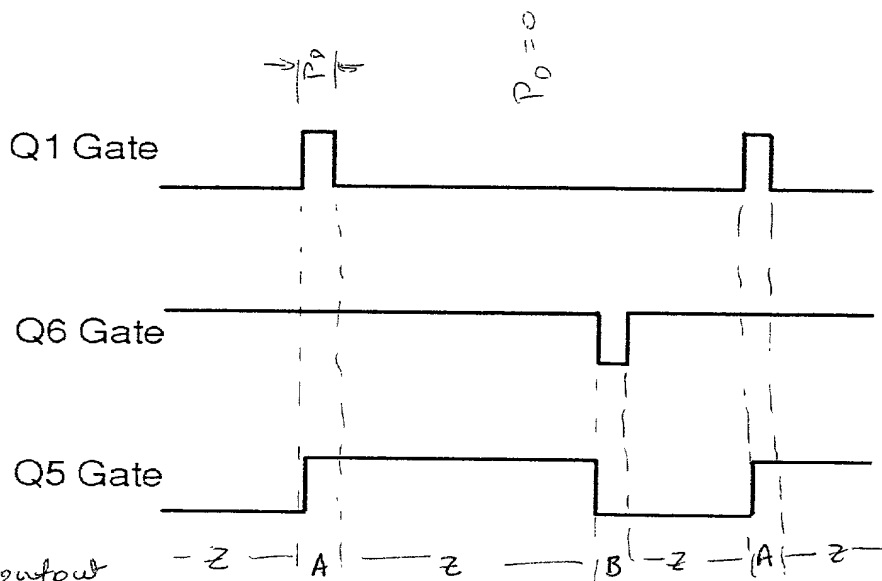


FIG 3



Q5 driver output action

Z - High output impedance

A - Current sourced to charge Q5 gate (charge transfer)

B - Current sink to discharge Q5 gate (charge removal)

FIG 4

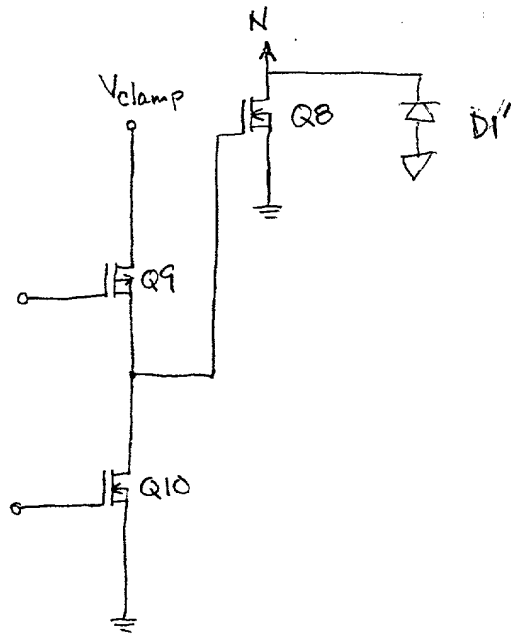


Fig 5

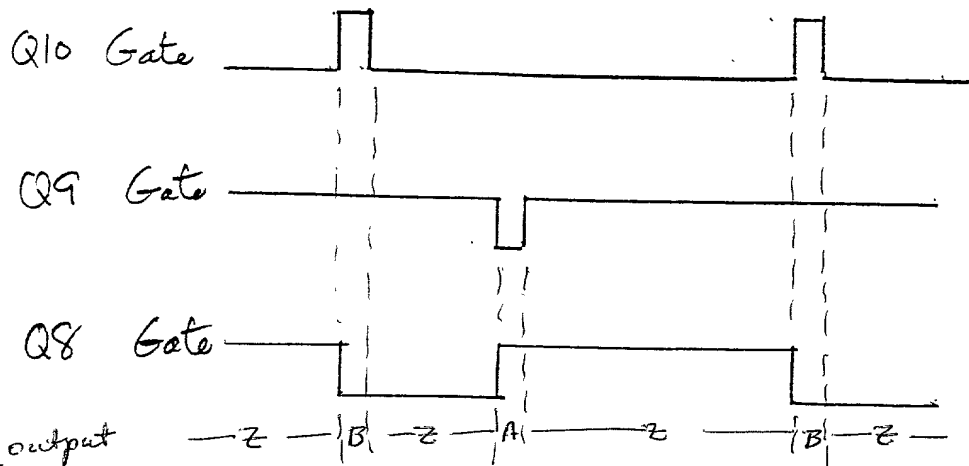
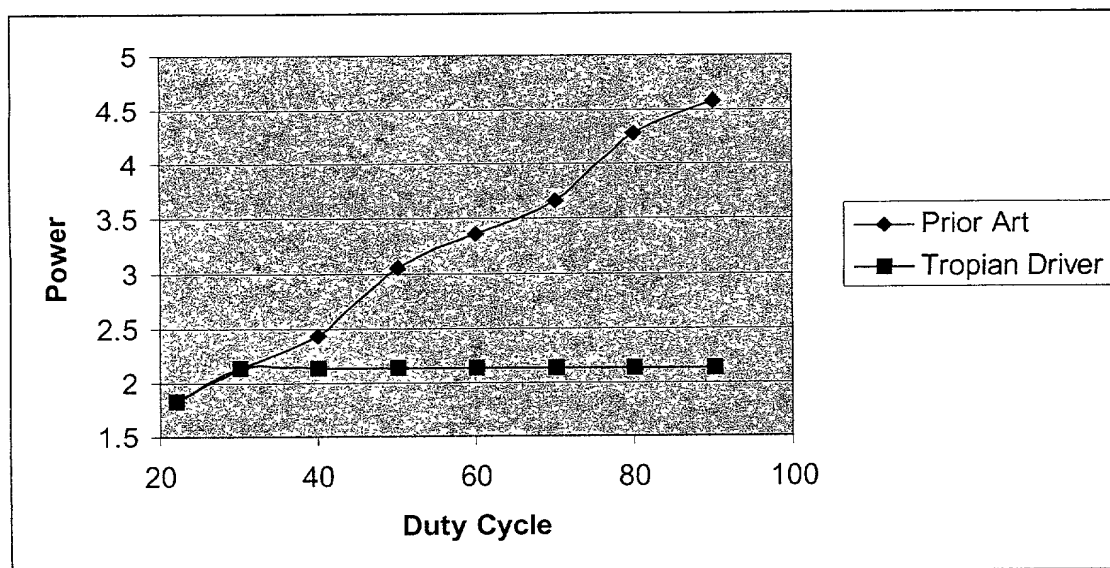


Fig 6



$$F = 2.4 \text{ MHz}$$

$$V_{DD} = 30.5 \text{ V}$$

FIG 7

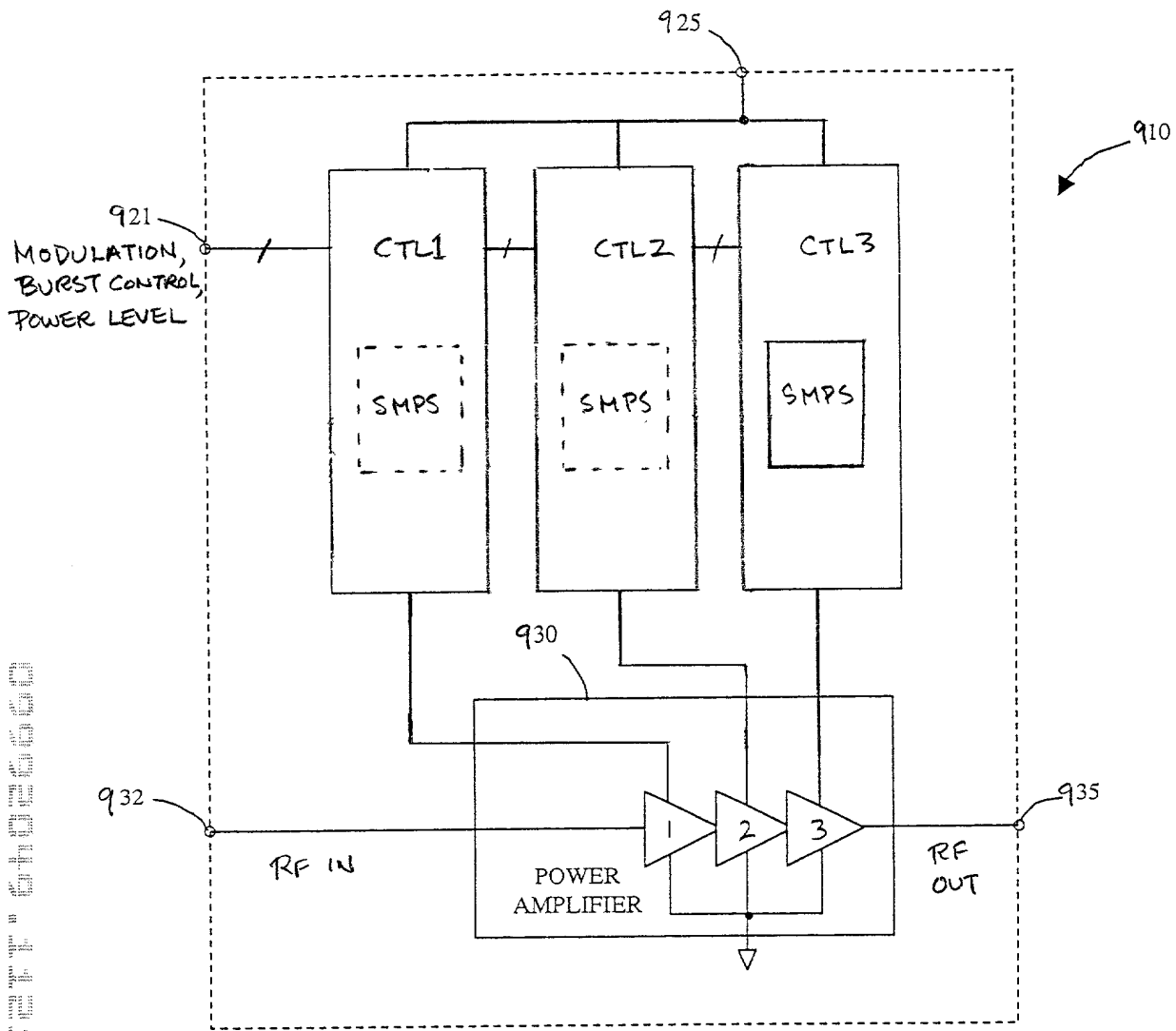
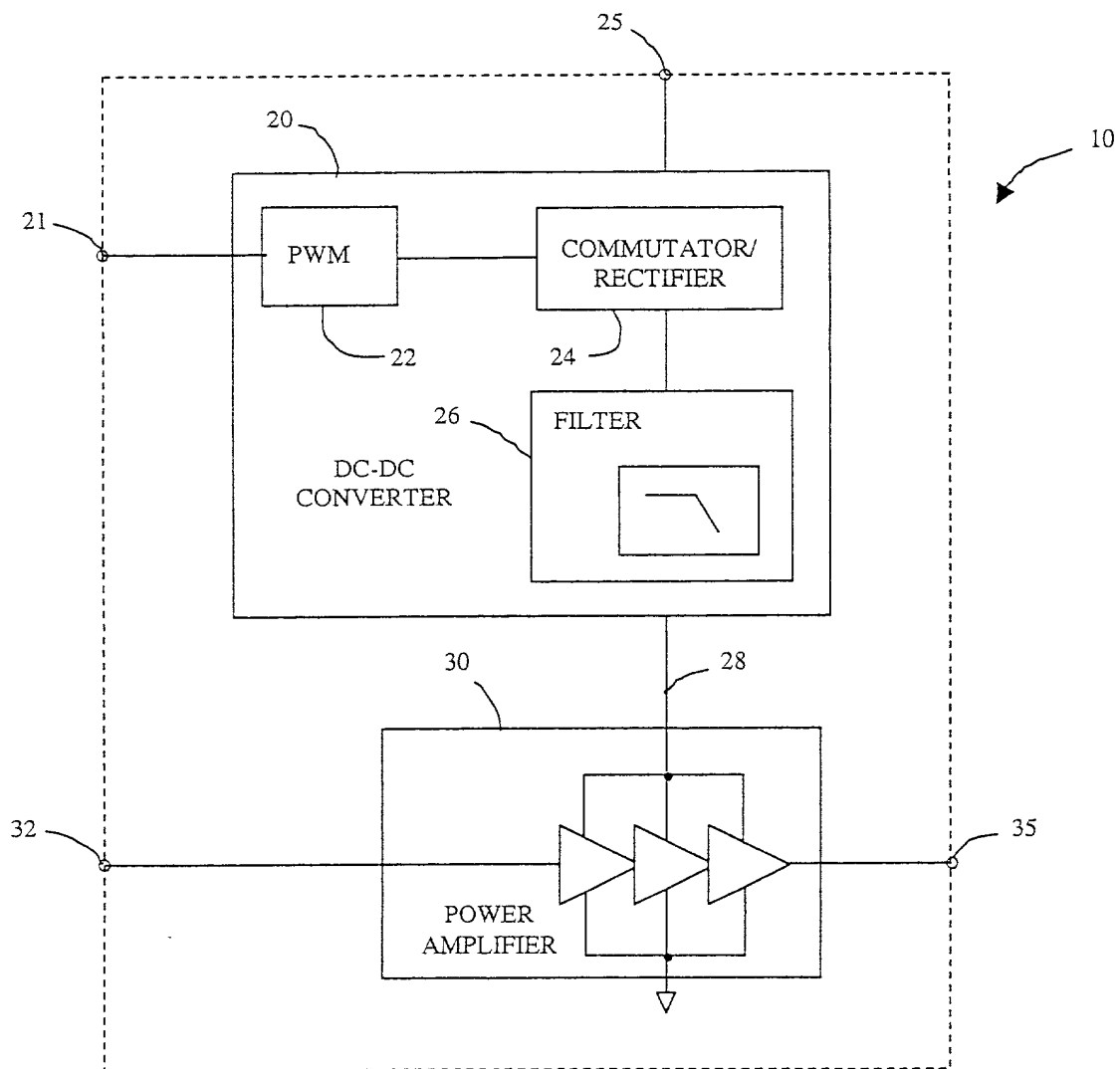


Fig. 8



*Fig. 9*  
(PRIOR ART)